

ENGINEERING MEMO

| Initiated by: | | | Dave Emrich | | | | | |
|--|--|---------------------------------|---|---|---------------------------------|---|---|--|
| Project Manager: | | | Tom Booler | | | | | |
| Proposed Priority: | | | Fast Track Normal | | | | | |
| Title: | RF ar | npli | ifiers in the A | SC mod | ule | e | | |
| Affected item(s): | | | | | | | | |
| All MWA re | eceivers a | t the | MRO, and possibly M | ike Kriel's AS | SC2.0 | .0 design. | | |
| Technical | descripti | on: | | | | | | |
| declared th chip at the | nat part o front end | bsole d of th | te and does not offer ne ASC is still in produ | a direct dro | p-in i he da | Cadjustable gain/attenuation stages has n replacement. (Note that the other amplifie date of this memo). and there are two ASCs in each receiver, | r | |
| The marke other chan spin of the Candidate | t must be ges to the circuit bo parts mus ce compa | exploe circulost bard is | ored to identify a can uit board. We have th s possible, but fairly u assessed on datashee | didate replace e Altium boa ndesirable (c | ceme ard d cost/ en te | .01 integrated circuits. nent part, the goals being to minimise any design files, so minor modifications and a ret/time/performance risk). tested in-circuit to confirm identical pency, noise figure, power consumption, hea | | |
| Effective Date: (dd-mm-yyyy) | | 2018-?-? | | | | | | |
| | | This issue is not yet resolved. | | | | | | |
| | | Unkn | Unknown, cheap if only new chips required, expensive if new board design required. | | | | | |
| Impact on Cu | | | Current impact minimal, but no spare AH101's so repairs of ASC modules is not currently possible. | | | | | |
| Other impacts: Unl | | Unkr | Jnknown. | | | | | |
| Attached | Documer | it(s): | | | | | | |
| | | | | | | | | |
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| Email: d.emrich@cu | | @cur | tin.edu.au | Date: | Ī | 2018-6-12 | | |